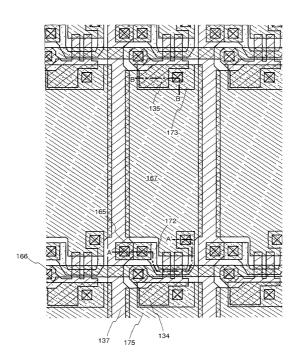
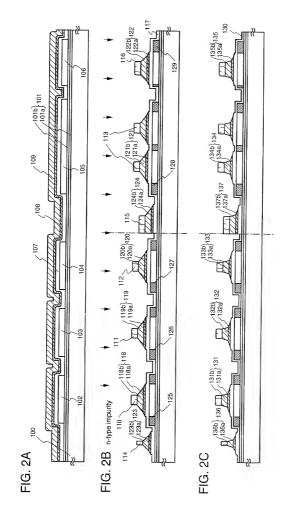
FIG. 1





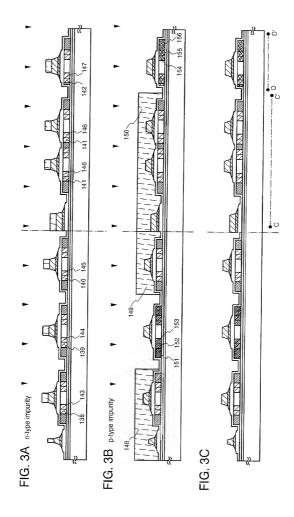
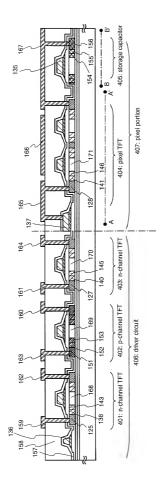


FIG. 4



-lG. 5

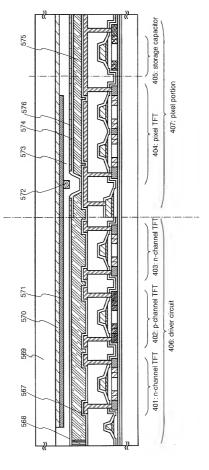
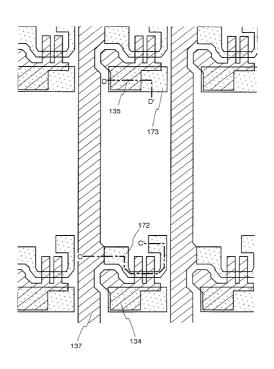


FIG. 6



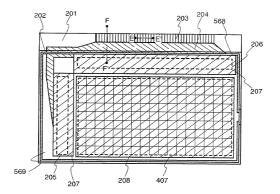


FIG. 7A top view

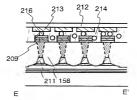


FIG. 7B cross sectional view of E-E'

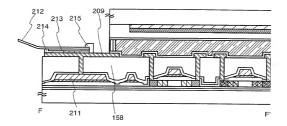


FIG. 8A cross sectional view of F-F'

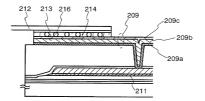
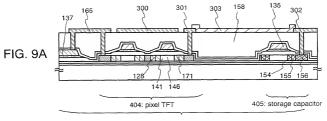
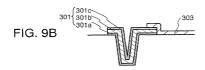


FIG. 8B



407: pixel portion



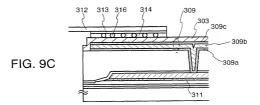


FIG. 10

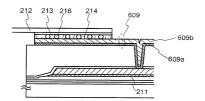


FIG 11

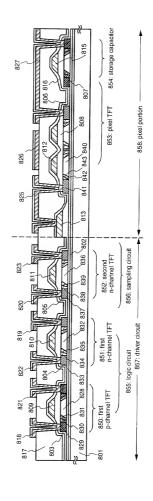


FIG. 12

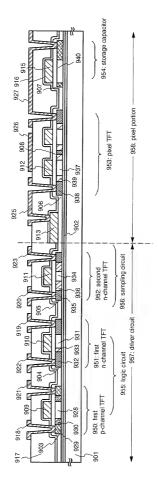


FIG. 13

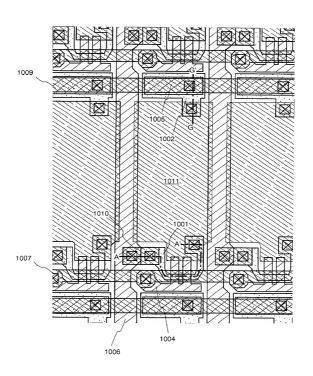


FIG. 14

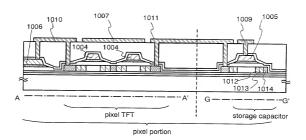


FIG. 15

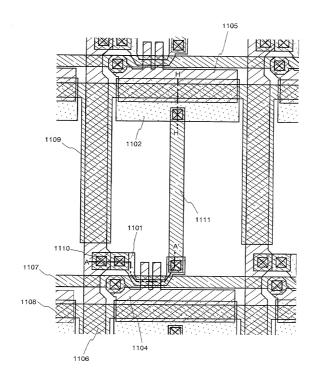


FIG. 16

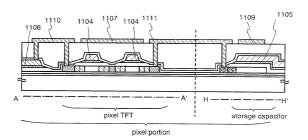


FIG. 17

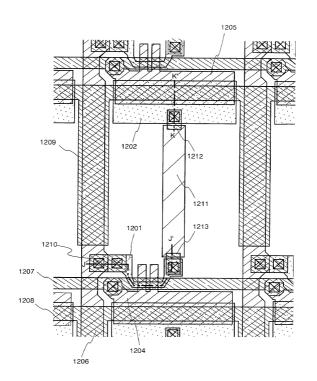


FIG. 18

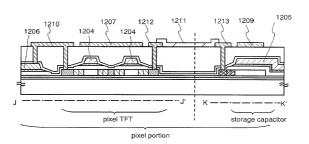
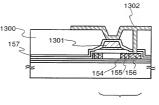


FIG. 19



405: storage capacitor

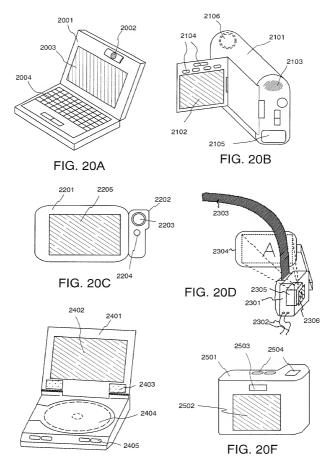
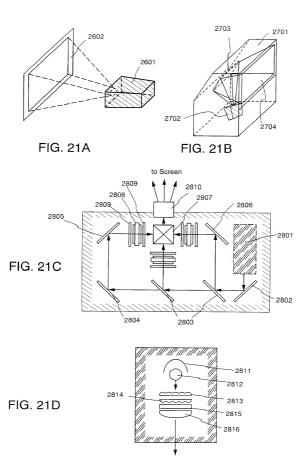
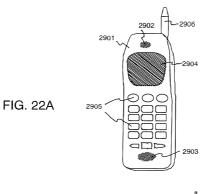
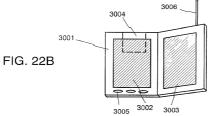


FIG. 20E







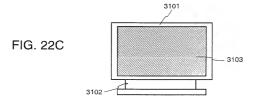


FIG. 23 prior art

